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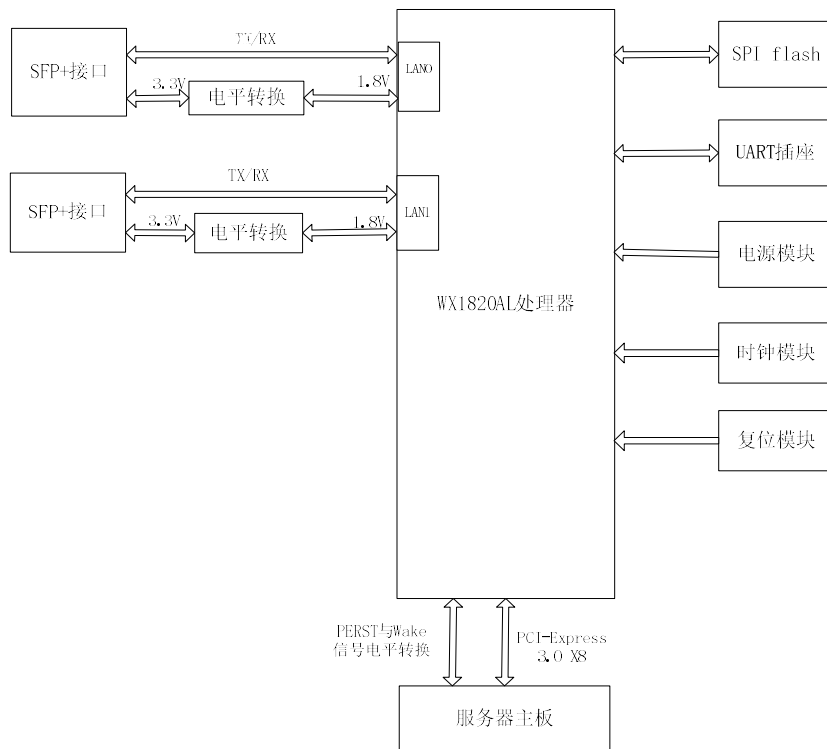
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**Note the single ended signal IO level standard of WX1820AL chip is 1.8V LVC MOS standard**

<b>Ball #</b>	<b>PinName</b>	<b>Type</b>	<b>Description</b>











<b>Ball #</b>	<b>PinName</b>	<b>Type</b>	<b>Description</b>

<b>Ball #</b>	<b>PinName</b>	<b>Type</b>	<b>Description</b>

<b>Ball #</b>		<b>NCSI PinName</b>	<b>Type</b>	<b>Description</b>

<b>Ball #</b>	<b>PinName</b>

Ball #	PinName	Type	Description

Ball #	PinName	Type	Description

Ball #	PinName	Type	Description

Ball #	PinName	Type	Description

Ball #	PinName	Type	Description

Ball #	PinName	Type	Description



<b>Ball #</b>	<b>Pin Name</b>	<b>Description</b>

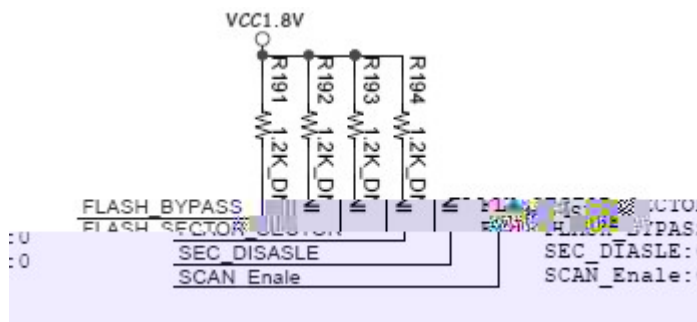
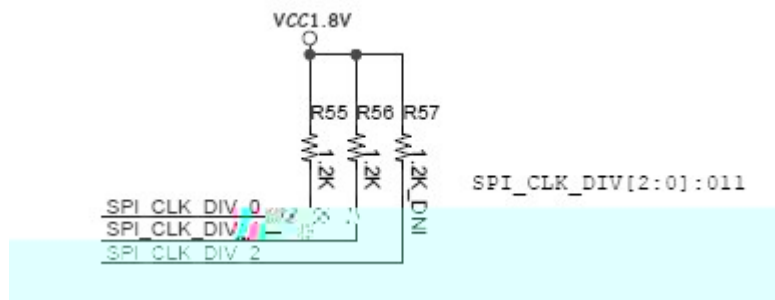
	<b>f</b>		<b>u</b>	<b>f p</b>

**Table 18 WX180AL**

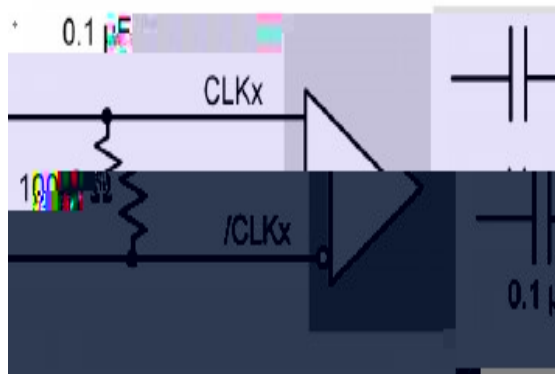
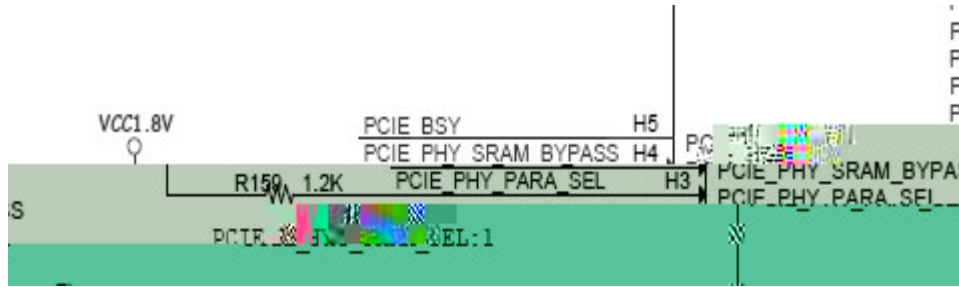
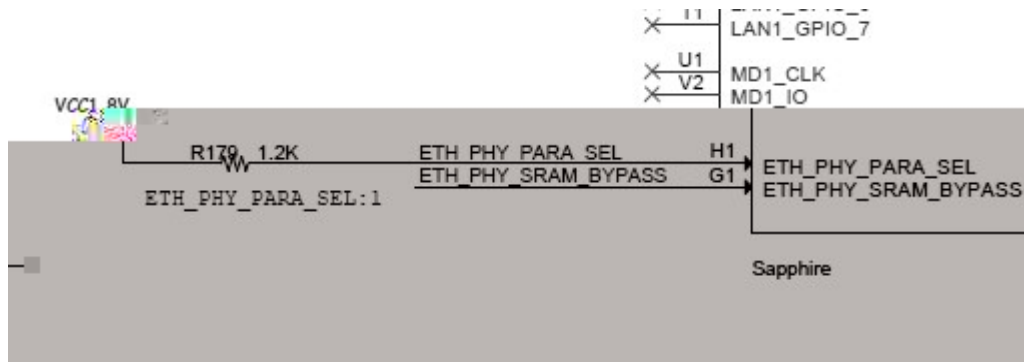
	<b>f</b>		<b>u</b>	<b>f p</b>

**Table 19 WX180AL**


Table 20 U

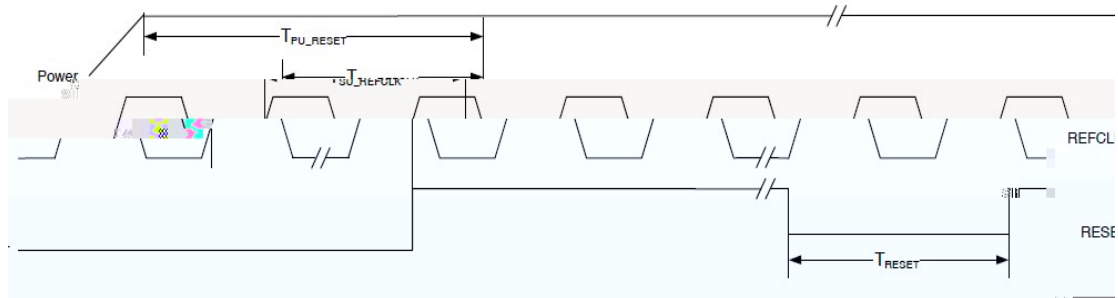






	<b>f</b>		<b>u</b>	<b>f p</b>

**Table 21**



<b>rc roo</b>	<b>la i</b>
<b>winbond</b>	<b>W25Q16FW</b>
<b>microchip</b>	<b>SST26WF080B</b>

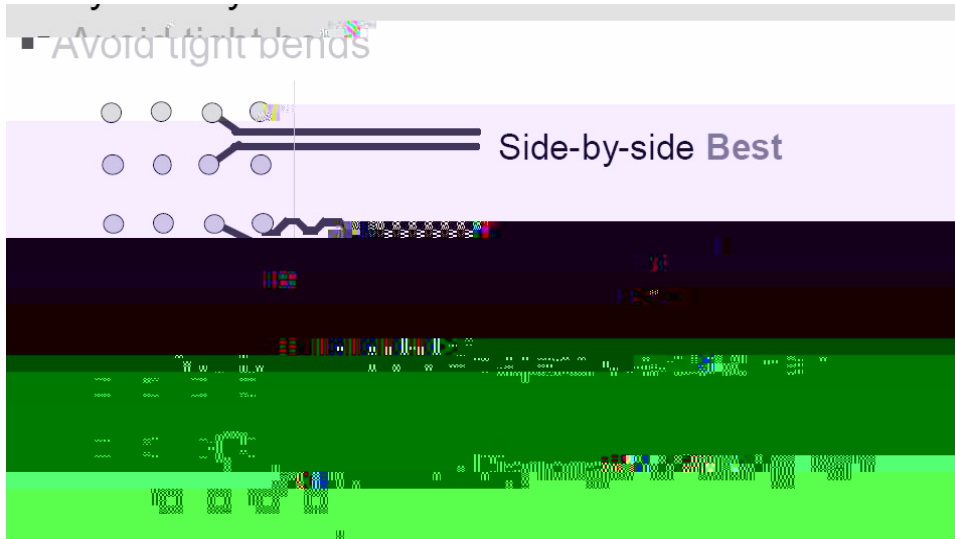
**Table 22**

<b>rc roo</b>	<b>la i</b>
<b>Avago</b>	<b>AFER-7096MZ</b>
<b>Intel</b>	<b>FILX8571D3BCV-IT</b>
<b>F-tone</b>	<b>FICS-851X 02Dex</b>
<b>Finisar</b>	<b>FILX8574D3BCL</b>

<b>Tixson</b>	<b>TPS- TGMB 85DCR</b>
<b>Huawei</b>	<b>CMND30000</b>
<b>HP</b>	<b>SR SFP+ 456096 001</b>
<b>Hasense</b>	<b>LIF8502- BC+</b>
<b>JDSU</b>	<b>PLR&amp;PL SC S43</b>
<b>WID</b>	<b>RIXM28 551</b>

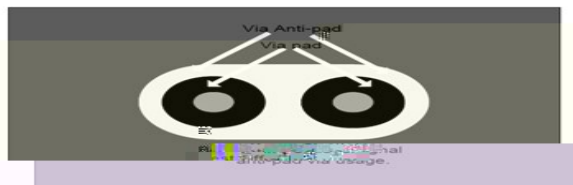
**Table 23**

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